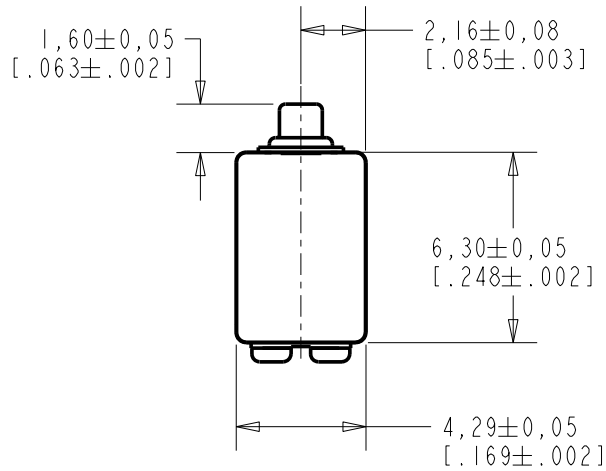


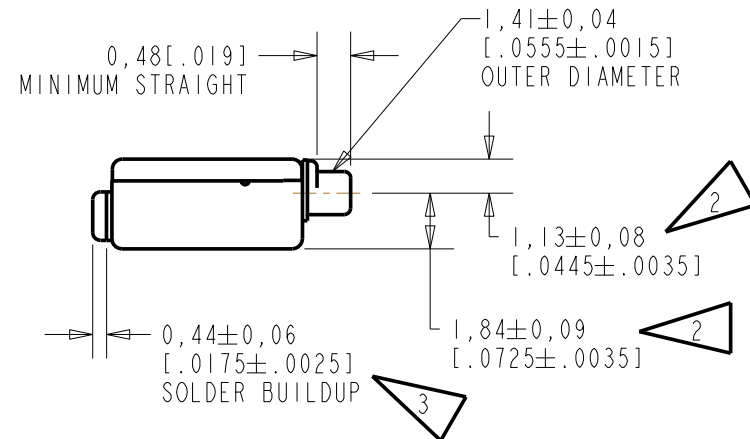
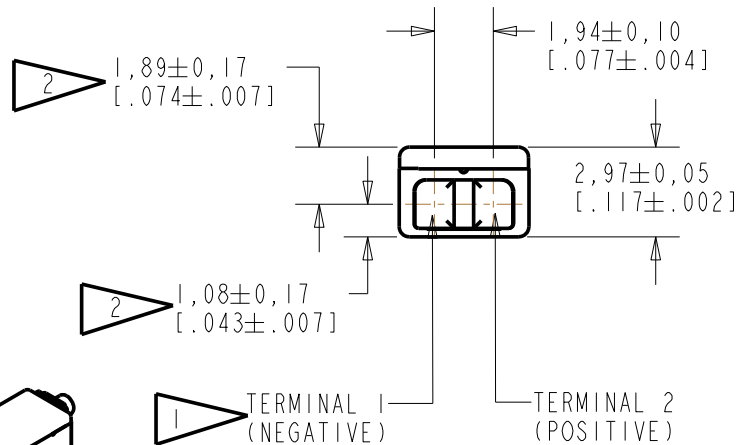
ED-26826-A67

SHT 1.1



NOTE:

- 1 A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL 1, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET.
- 2 LOCATED FROM TWO SURFACES FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED TO  $\pm 0,17$  [.007].
- 3 FROM TOP OF TERMINAL



SCALE 2:1

NOMINAL WEIGHT  
.31 GRAM

DIMENSIONS IN MILLIMETERS [INCHES]

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
C	C10107990	8-25-08	Active	C
B	C10105840	5-14-07		
A	C10103546	1-6-06		

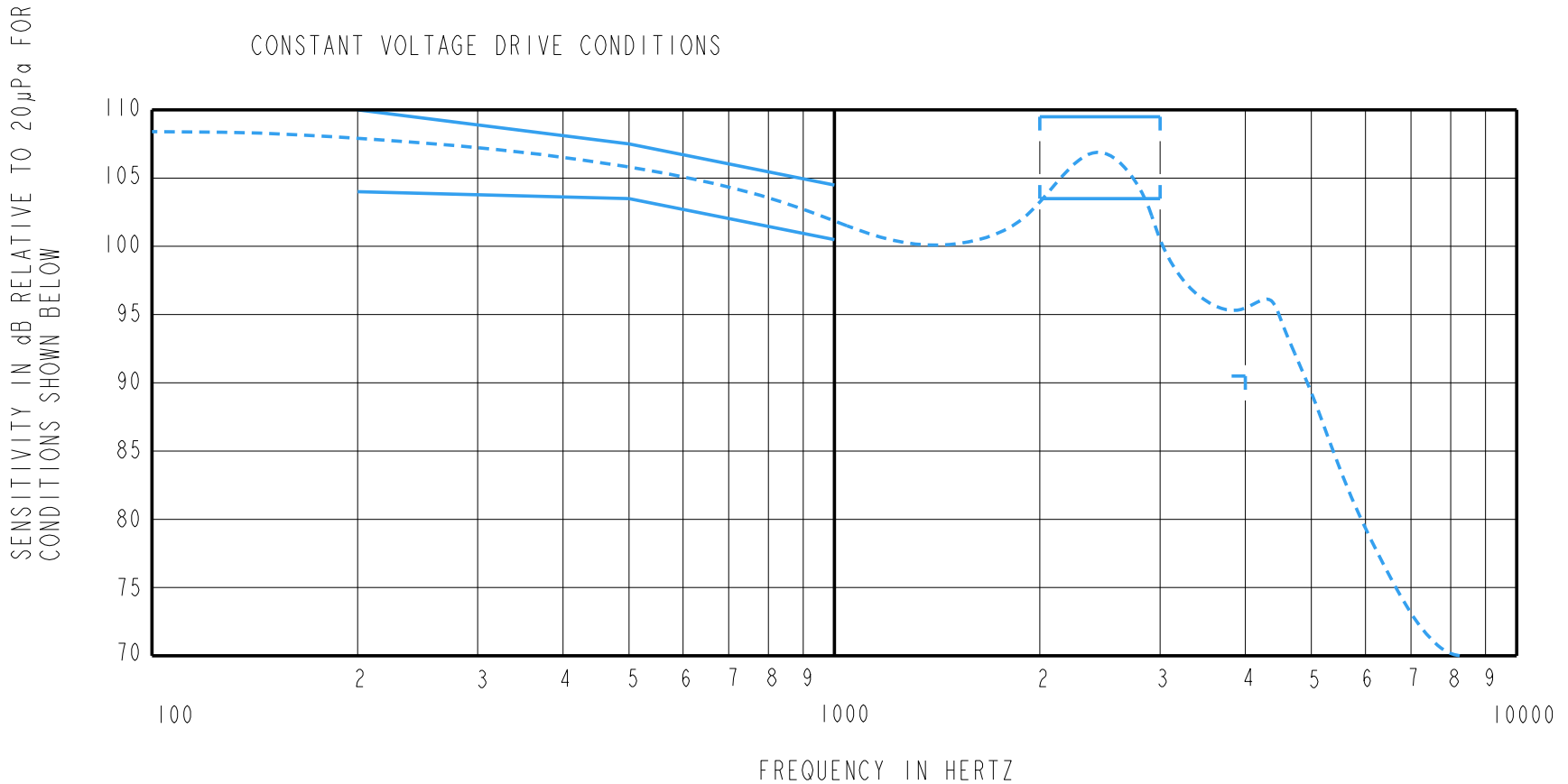
SCALE: 4:1		DR. BY	DATE
DO NOT SCALE DRAWING		CRG	1-6-06
TITLE: RECEIVER		ED-26826-A67	CK. BY
OUTLINE DRAWING		SHT 1.1	GJP
		APP. BY	DATE
		GJP	1-9-06
		APP. BY	DATE
		GJP	1-9-06

**KNOWLES ELECTRONICS**  
ITASCA, ILLINOIS U.S.A.

THE ED-26826-A67 IS A MAGNETIC BALANCED ARMATURE RECEIVER INTENDED FOR USE IN ITE HEARING INSTRUMENTS. THIS MODEL IS SHOCK PROTECTED AND HAS MEDIUM IMPEDANCE

TYPE II DAMPING

ED-26826-A67  
SHEET 2.1



ACOUSTICAL

SENSITIVITY  
DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 3.  
NOMINAL SENSITIVITY AT 1kHz IS dB RELATIVE TO 20µPa. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT 1kHz.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
200	+1.5	+4.5	+7.5
500	+1.0	+3.0	+5.0
1000	-2.0	102.5	+2.0
2000 - 3000	+1.0	+4.0	+7.0
4000	-12.0	---	---

PORT LOCATION: 12C

TABLE 1

TOTAL HARMONIC DISTORTION  
DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (mA)	LIMIT (%)
500	0.69	0	10
830	0.25	0	5
1250	0.25	0	5
2100	0.71	0	10

TABLE 2

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.25 V rms, 0mA DC BIAS
SOURCE IMPEDANCE	<1 Ohm
TUBING	10mm [.394"] LONG, 1mm [.039"] I.D.
COUPLER CAVITY	2 CM <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 126)

TABLE 3

ELECTRICAL

DC RESISTANCE	61 OHMS ± 10%
IMPEDANCE @ 500 Hz	124 OHMS ± 15%
IMPEDANCE @ 1 kHz	222 OHMS ± 15%

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

SUFFIX DESCRIPTION:

- 1) ASSEMBLE WITH "RIBBED" AND TAPERED COIL.
- 2) APPLY SOLDER TO HEIGHT SHOWN ON SHEET 1.1.
- 3) RIB STRIKE TEST: (DISTORTION TEST) +9 dB DRIVE @ MOTOR RESONANCE.

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
C	C10107990	8-25-08	Active	C
B	C10105840	5-14-07		
A	C10103546	1-6-06		

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WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION

TITLE: **RECEIVER**  
PERFORMANCE SPECIFICATION

**ED-26826-A67**  
SHT 2.1

DR. BY	DATE
CRG	1-6-06
CK. BY	DATE
GJP	1-9-06
APP. BY	DATE
GJP	1-9-06